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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/736,651	12/13/2000	Bart Dierickx	522-1729	8263
23644	7590	09/19/2005	EXAMINER	
BARNES & THORNBURG P.O. BOX 2786 CHICAGO, IL 60690-2786			LEE, EUGENE	
			ART UNIT	PAPER NUMBER
			2815	

DATE MAILED: 09/19/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.		Applicant(s)	
	09/736,651		DIERICKX, BART	
	Examiner		Art Unit	
	Eugene Lee		2815	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 08 July 2005.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-5 and 7-24 is/are pending in the application.
- 4a) Of the above claim(s) 2-5, 11-24 is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1 and 7-10 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Objections

1. Claim 1 is objected to because of the following informalities: in line 11 of said claim, there is a typographical error in the limitation "the region is ... substrate". There appears to be an article missing (i.e. part of the substrate) before the limitation "substrate". Appropriate correction is required.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

3. Claims 1, 7, 8, and 10 are rejected under 35 U.S.C. 102(e) as being anticipated by Rhodes 6,204,524. Rhodes discloses (see, for example, FIG. 5) a pixel cell (pixel structure) 114 comprising a substrate, radiation sensitive source of carriers in the substrate, transfer region (region in the substrate for collecting but not storing carriers) 126, floating region of a second conductivity type (one doped or inverted region) 130, and one planar current flow, carrier transport pathway.

Regarding the limitation "wherein the region for collecting but not storing carriers is substrate under a polysilicon gate electrode", see column 8, lines 41-51 wherein Rhodes

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discloses the conductive layer (polysilicon gate electrode) 108 comprising polysilicon and part of transfer gate stack 128.

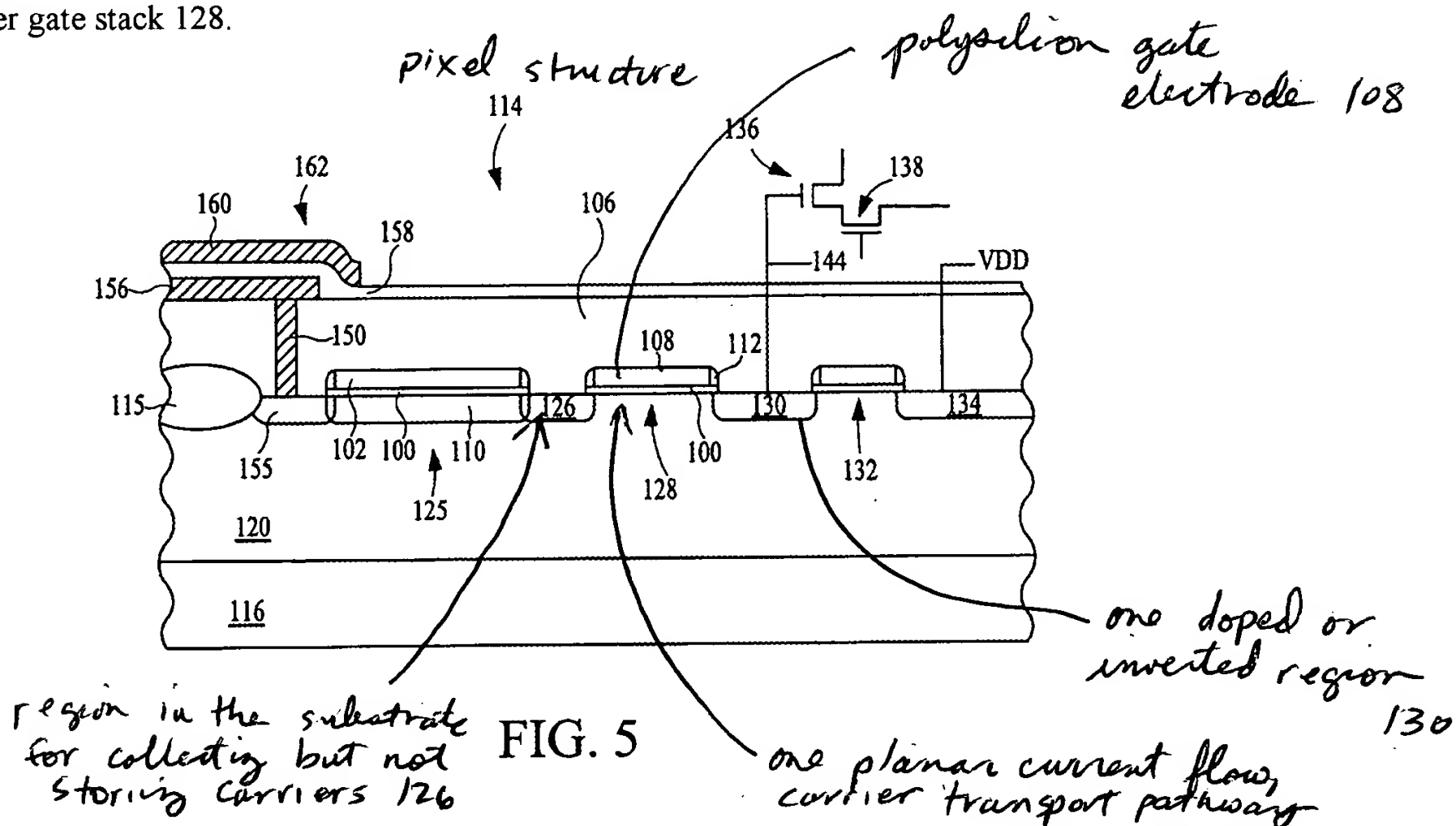


FIG. 5

Regarding the limitation “radiation sensitive source of carriers in the substrate”, a CMOS imager is a device containing a pixel cell that takes photo-generated charge and transfers it to a floating diffusion region. See, for example, column 2, lines 1-32 of Rhodes.

Regarding the limitation “a region in the substrate for collecting but not storing carriers”, Rhodes discloses (see, for example, column 9, lines 19-23) the transfer region 126 having a dopant concentration of 1×10^{15} ions/cm² – 1×10^{16} ions/cm². On page 7, lines 26-33, the applicant discloses the concentration of the carrier collecting region 3 being 1 to 2×10^{16} /cm² wherein such a low concentration means that no free charges are available, i.e. no storage of charge.

Regarding claim 8, see, for example, FIG. 5 wherein Rhodes discloses doped region (at least one implant) 110.

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Claim Rejections - 35 USC § 103

4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

5. Claim 9 is rejected under 35 U.S.C. 103(a) as being unpatentable over Rhodes '524 B1 as applied to claims 1, 7, 8, and 10 above, and further in view of Baukus et al. 5,973,375. Rhodes discloses (see, for example, FIG. 5) a field oxide layer (field oxide) 115. Rhodes does not disclose regions of a second conductivity type in or on the substrate avoiding touching of the region for collecting but not storing carriers and a field oxide. However, Baukus discloses (see, for example, FIG. 11) a semiconductor device comprising a buried layer 70. In column 9, lines 41-46, Baukus discloses the buried layer inhibiting current flow between regions. Therefore, it would have been obvious to one of ordinary skill in the art at the time of invention to have regions of a second conductivity type in or on the substrate avoiding touching of the region for collecting but not storing carriers and a field oxide in order to inhibit current flow between regions.

Response to Arguments

6. Applicant's arguments with respect to claims 1, and 7-10 have been considered but are moot in view of the new ground(s) of rejection.

Regarding the applicant's argument on page 2 of the response filed 7/8/05 that Rhodes does not disclose that the region for collecting but no storing carriers is part of the substrate

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under a polysilicon gate, this argument is not persuasive. In FIG. 5, Rhodes clearly discloses the region 126 under the polysilicon gate electrode 108. In Webster's Collegiate Dictionary, the word "under" is defined as 1 : in or into a position below or beneath something. Clearly, the region 126 is into a position below the polysilicon gate electrode as the edge of region 126 is under the polysilicon gate electrode 108.

Conclusion

THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the mailing date of this final action.

INFORMATION ON HOW TO CONTACT THE USPTO

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Eugene Lee whose telephone number is 571-272-1733. The examiner can normally be reached on M-F 8-5.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tom Thomas can be reached on 571-272-1664. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Eugene Lee
April 2, 2005

A handwritten signature in black ink, appearing to be 'Eugene Lee', written in a cursive style.